WEST

| Help | Logout | Interrupt |

| Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers | Preferences | Cases |

Search Results -

Terms	Documents
L20 and (hit near4 (rate or ratio or percentage))	28

Database:
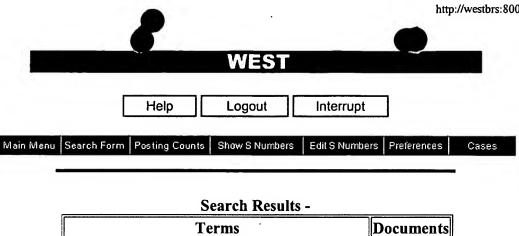
US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L29

| Refine Search |

| Recall Text ⬍ | Clear |

Search History

DATE: Friday, November 14, 2003   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=ADJ		
L29	L20 and (hit near4 (rate or ratio or percentage))	28	L29
	DB=PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
L28	L26 and (hit near4 (rate or ratio or percentage))	5	L28
L27	L26 and (hit near4 (rate or ratio))	4	L27
L26	L25 and (cache near6 allocat$4 with (host or processor or CPU or process or thread or (central adj processing adj unit)))	20	L26
L25	L24 and l23 and l22	177	L25
L24	(((cache or "L2") adj2 control$4) or ((fetch or decode) adj2 (unit or section))) with (host or processor or CPU or process or (central adj processing adj unit))	2204	L24
L23	(multi-processor or multiprocessor or ((multiple or several or plurality or second) adj2 (host or processor or CPU or (central adj processing adj unit))))	32879	L23

WEST

Freeform Search

Database:
- US Patents Full-Text Database
- US Pre-Grant Publication Full-Text Database
- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Term:

Display: `10` Documents in **Display Format:** `TI` **Starting with Number** `1`

Generate: ○ Hit List ⦿ Hit Count ○ Side by Side ○ Image

| Search | Clear | Help | Logout | Interrupt |

| Main Menu | Show S Numbers | Edit S Numbers | Preferences | Cases |

Search History

DATE: **Friday, November 14, 2003** 　 Printable Copy 　 Create Case

Set Name	Query	Hit Count	Set Name
side by side			result set
	DB=USPT; PLUR=YES; OP=ADJ		
L3	L1 and (((repartition$4 or reallocat$4 or allocat$4 or distribut$4 or assign$4 or redistribut$4) near6 (cache or partition or block or way)) same (boot$4 or reboot$4 or power$4 or (turn$3 on)))	26	L3
L2	L1 and ((repartition$4 or reallocat$4) with (boot$4 or reboot$4 or power$4 or (turn$3 on)))	0	L2
L1	cache and ((operating adj2 system) with (execut$4 or run$4) with (partition or section or portion))	457	L1

END OF SEARCH HISTORY

L22	(multiple or several or plurality or second or another) adj2 (cache or "L2")	3743	L22

DB=USPT; PLUR=YES; OP=ADJ

L21	L20 and (hit near4 (rate or ratio))	23	L21
L20	L18 and (cache near6 allocat$4 with (host or processor or CPU or process or thread or (central adj processing adj unit)))	89	L20
L19	L18 and (cache near6 allocat$4 with (host or processor or CPU or process or (central adj processing adj unit)))	89	L19
L18	L17 and l14	194	L18
L17	(((cache or "L2") adj2 control$4) or ((fetch or decode) adj2 (unit or section))) with (host or processor or CPU or process or (central adj processing adj unit))	4243	L17
L16	(((cache or "L2") adj2 control$4) or ((fetch or decode) adj2 (unit or section)))	9701	L16
L15	L14 and (((cache or "L2") adj2 control$4) or ((fetch or decode) adj2 (unit or section)))	258	L15
L14	L13 and l1	349	L14
L13	(multiple or several or plurality or second or another) adj2 (cache or "L2")	5576	L13
L12	L11 and ((alloca$4 or free$3 or available) near6 (block or line location))	20	L12
L11	L9 and (control$4 with (access$4 or read$4 or writ$4 or store or load$4 or fetch$4) with (block or line location))	68	L11
L10	L9 and (control$4 near6 (access$4 or read$4 or writ$4 or store or load$4 or fetch$4) near8 ((allocable or free$3 or available) near4 (block or line location)))	0	L10
L9	L2 and ((multiple or several or plurality or second or another) adj2 ((cache or "L2") adj3 control$4))	203	L9
L8	L2 and ((multiple or several or plurality or second) adj2 ((cache or "L2") adj3 control$4))	203	L8
L7	L2 and ((multiple or several or plurality or second) adj2 (cache adj3 control$4))	202	L7
L6	L5 and ((cache or block) near4 allocat$4)	53	L6
L5	L4 and ((host or processor or CPU or (central adj processing adj unit)) with (cache adj2 control$4))	78	L5
L4	L3 and ((monitor$4 or check$4 or determin$4 or record$4 or control$4 or observ$4 or supervis$4 or track$4 or watch$4) near6 (cache near4 (hit or performance)))	110	L4
L3	L2 and L1	332	L3
L2	(multi-processor or multiprocessor or ((multiple or several or plurality or second) adj2 (host or processor or CPU or (central adj processing adj unit))))	34433	L2
L1	(cache near3 (partition$4 or block or line or section$4 or area or portion$4 or divi$4)) near8 (allocat$4 or distributed or appropriat$4 or designat$4 or select$4) near8 (processor or CPU or host or process)	549	L1

Set Name	Query	Hit Count	Set Name
side by side			result set
	DB=USPT; PLUR=YES; OP=ADJ		
L10	L8 and ((partition$4 or section$4 or portion$4 or divi$4 or part$4) near8 (cache or block))	50	L10
L9	L8 and ((partition$4 or section$4 or portion$4 or divi$4 or part$4) near8 cache)	50	L9
L8	L7 and (cache near8 (performance or rate or ratio or measur$4))	51	L8
L7	L6 and ((control$4 or manag$4 or arbit$6) near6 (("l2" or second or secondary or lower) adj2 cache))	65	L7
L6	L5 and (((allocat$4 or assign$4 or appropriat$4 or designat$3) near4 (block or cache or location or line)) with (host or processor or CPU or (central adj processing adj unit)))	184	L6
L5	L4 and ((control$4 or manag$4 or arbit$6) with (interconnect$3 or link$4 or bus or channel))	348	L5
L4	L3 and l2 and l1	382	L4
L3	((allocat$4 or assign$4 or appropriat$4 or designat$3) near4 (block or cache or location or line)) with (cache near4 (control$4 or manag$4 or arbit$6))	993	L3
L2	(host or processor or CPU or (central adj processing adj unit)) with (cache near4 (control$4 or manag$4 or arbit$6))	4915	L2
L1	multi-processor or ((multiple or several or plurality) near4 (processor or CPU or (central adj processing adj unit)))	40798	L1

END OF SEARCH HISTORY

Set Name	Query	Hit Count	Set Name
side by side			result set

DB=USPT; PLUR=YES; OP=ADJ

Set Name	Query	Hit Count	Set Name
L14	l13 and (tag near4 (memory or storage or RAM))	26	L14
L13	L12 and (("L2" or upper or second or secondary) near3 cache)	62	L13
L12	L11 and (cache near8 (hit or miss or ratio or rate or performance or monitor$4))	74	L12
L11	L10 and ((control$4 or manag$4 or arbit$4) with (interconnect$3 or link$4 or bus or channel))	76	L11
L10	L9 and ((cache or system) near8 (part$4 or partition$4 or divi$4 or portion$4 or section$4 or repartition$4 or arrang$4))	81	L10
L9	L8 and l7 and l6 and l1	87	L9
L8	block with (allocat$4 or assign$4 or appropriat$4 or designat$4) with (process or processor or CPU or host) .	8596	L8
L7	(host or processor or CPU or (central adj processing adj unit)) with (cache near4 (control$4 or manag$4 or arbit$6))	4915	L7
L6	multi-processor or multiprocessor or ((multiple or several or plurality or second) adj2 (host or processor or CPU or (central adj processing adj unit)))	34433	L6
L5	L4 and ((("L2" or upper or second or secondary) near3 cache) near4 control$4)	26	L5
L4	L3 and (block with (allocat$4 or assign$4 or appropriat$4 or designat$4) with (process or processor or CPU or host))	67	L4
L3	L2 and ((cache or system or block) near8 (partition$4 or section$4 or portion$4 or divi$4 or repartition$4 or part$4))	384	L3
L2	L1 and ((block or line) with (interconnect$3 or link$4 or bus or channel) with control$4) and (((host or processor or CPU or (central adj processing adj unit)) with (cache near4 (control$4 or manag$4 or arbit$6))))	426	L2
L1	((711/122 |711/118 |711/119 |711/120 |711/129 |711/133 |711/143)!.CCLS.)	2335	L1

END OF SEARCH HISTORY